## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

bplication of: Barrie Gilbert

Serial No .:

09/675,902

Examiner: Tung X. Nguyen

Filed:

September 28, 2000

Group Art Unit: 2829

For:

GAIN AND PHASE DETECTOR HAVING DUAL LOGARITHMIC

**AMPLIFIERS** 

Date:

March 19, 2003

**BOX DD** 

ASSISTANT COMMISSIONER FOR PATENTS

Washington, DC 20231

# SUPPLEMENTAL INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Modified)

## **U.S. PATENT DOCUMENTS**

Exam <u>Ref</u> <u>Init</u>

Document Number

Issue **Date** 

<u>Name</u>

Sub-

Class Class

### FOREIGN PATENT DOCUMENTS

Document

Publication

Number

**Date** 

Country

Name

### OTHER DOCUMENTS

Exam

Ref

(Including Author, Title, Date, Pertinent Pages, etc.

<u>Init</u> DV

Monolithic Logarithmic Amplifiers article by Barrie Gilbert, Analog

Devices, Inc., August 1994, pp. 1-122

Examiner:

Date Considered: 5/16/83

RECEIVED 1003 2000 RECHIVED 2000